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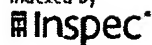
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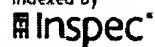
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Lin, C.E.; Chih-Ching Li; Chih-Chen Wu; Liu, H.S.; Tseng, M.Y.;
Industrial Electronics Society, 2003. IECON '03. The 29th Annual Conference
Volume 2, 2-6 Nov. 2003 Page(s):1228 - 1234 Vol.2
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 Acquaviva, A.; Benini, L.; Ricco, B.;
 Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction:
 Volume 20, Issue 11, Nov. 2001 Page(s):1283 - 1292
 Digital Object Identifier 10.1109/43.959857
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 Martin, T.L.; Siewiorek, D.P.;
 Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
 Volume 9, Issue 1, Feb. 2001 Page(s):29 - 34
 Digital Object Identifier 10.1109/92.920816
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| <input type="checkbox"/> | <p>3. Processor frequency setting for energy minimization of streaming multir
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 Volume 18, Issue 3, Aug-Sep 1999 Page(s):6 - 9
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- ☐ 7. **Overview latest technologies using heat pipe and vapor chamber for cool generation notebook computer**
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- ☐ 10. **Modeling and performance analysis of single-bus tightly-coupled multipr**
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Mirfakhraei, N.;
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- ☐ **21. Multiplier-free realizations for FIR multirate converters based on mixed- π representation**
Li, J.; Tantarana, S.;
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- ☐ **22. A multi-channel ADC for use in the PHENIX detector**
Emery, M.S.; Frank, S.S.; Britton, C.L., Jr.; Wintenberg, A.L.; Simpson, M.L.; E Young, G.R.; Clonts, L.G.; Allen, M.D.;
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Volume 44, Issue 3, Part 1, June 1997 Page(s):374 - 378
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- ☐ **23. An area-efficient VLSI architecture of a Reed-Solomon decoder/encoder I**
Sunghoon Kwon; Hyunchul Shin;
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- ☐ **24. Design of a pipelined music synthesizer based on the wavetable method**
Min-Ho Hyun; Jong-Hyun Na; Sun-Young Hwang;
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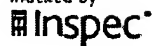
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- ☐ 1. Intelligent yield and speed prediction models for high-speed microproces
Tae Seon Kim;
Electronic Components and Technology Conference, 2002. Proceedings. 52nd
28-31 May 2002 Page(s):1158 - 1162
Digital Object Identifier 10.1109/ECTC.2002.1008251
[AbstractPlus](#) | Full Text: [PDF\(559 KB\)](#) IEEE CNF
- ☐ 2. A digital chip for robust speech recognition in noisy environment
Chang-Min Kim; Soo Young Lee;
Acoustics, Speech, and Signal Processing, 2001. Proceedings. (ICASSP '01).
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Volume 2, 7-11 May 2001 Page(s):1089 - 1092 vol.2
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